

Single-In-Line, Coated, 4 Bits to 8 Bits

R/2R Ladder Networks

APPLICATIONS

R/2R Ladder networks for D/A and A/D converter with bi-polar or CMOS switches

ELECTRICAL SPECIFICATIONS

Ladder Network Accuracy on Linearity: $\pm 1/2$ LSB.

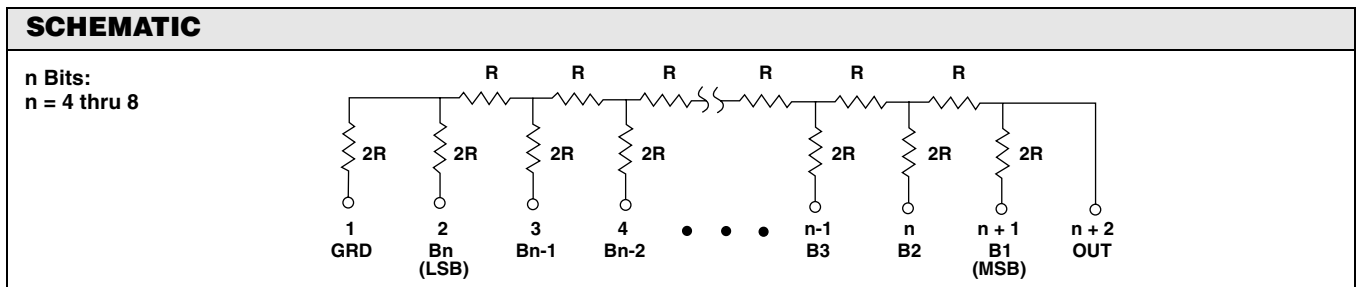
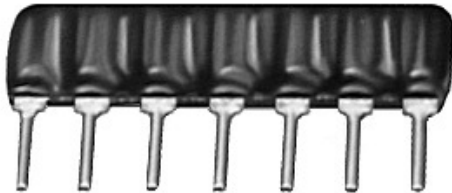
Ladder Network Resistance Tolerance: $\pm 2\%$.

Temperature Coefficient of Resistance: ± 100 PPM/ $^{\circ}$ C.

Operating Temperature Range: -55° C to $+125^{\circ}$ C.

Power Dissipation Rating at $+70^{\circ}$ C Ambient:
50 mW/element.

Standard Resistance Values (R): 5 kilohms, 10 kilohms, 25 kilohms, 50 kilohms and 100 kilohms.



DIMENSIONAL CONFIGURATIONS

in inches [millimeters]

NUMBER OF PINS	A (Max.)	B ± 0.005 [0.127]	C (Max.)
6	0.590 [14.99]	0.500 [12.70]	0.350 [8.89]
7	0.690 [17.53]	0.600 [15.24]	0.350 [8.89]
8	0.790 [20.07]	0.700 [17.78]	0.350 [8.89]
9	0.890 [22.61]	0.800 [20.32]	0.350 [8.89]
10	0.990 [25.15]	0.900 [22.86]	0.350 [8.89]

HOW TO ORDER

T10S MODEL	08 NUMBER OF BITS	104 RESISTANCE VALUE (Ohms)	e2 LEAD TERMINATION
T06S = 6 pin SIP T07S = 7 pin SIP T08S = 8 pin SIP T09S = 9 pin SIP T10S = 10 pin SIP	6 pin = 4 Bits 7 pin = 5 Bits 8 pin = 6 Bits 9 pin = 7 Bits 10 pin = 8 Bits	First two digits are significant, third digit signifies number of zeros to follow. EXAMPLE: 104 = R = 100 kilohms. REFERENCE: 2R = 200 kilohms.	e2 = SnAg 95/5